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PATENT APPLICATION

Technology Center 2100 ATTORNEY DOCKET NO. 71101

RS
4IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2-1-02

Applicants: Harel PRIMACK et al.)

CERTIFICATE OF MAILING

Appln. No.: 09/829,838)

I hereby certify that this paper is being
deposited with the United States Postal Service
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Washington, D.C. 20231, on this date.

Mailing Date: April 10, 2001)

Title: DIGITAL PHASE LOCKED LOOP)

09/10/01

Date Registration No. 25,947
Attorney for Applicant(s)

Group)

Art Unit: 2183)

Examiner: not yet known)

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Commissioner of Patents and Trademarks
Technology Center 2600ATTENTION: Assistant Commissioner for Patents
Washington, D.C. 20231

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This is an Information Disclosure Statement being submitted in accordance with 37 C.F.R. §§ 1.97 - 1.98 in connection with the above-identified application. Submitted herewith is Form PTO-1449 and a copy of the documents thereon are enclosed.

U.S. PATENTS

<u>Patent No.</u>	<u>Inventor</u>	<u>Issue Date</u>
4,285,044	Thomas et al.	Aug. 18, 1981
4,761,751	Canniff	Aug. 2, 1988
4,888,719	Yassa	Dec. 19, 1989
4,905,177	Weaver, Jr. et al.	Feb. 27, 1990
5,113,361	Damerow et al.	May 12, 1992
5,631,586	Sogo	May 20, 1997

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OCT 10 2001
2800 MAIL ROOM

OTHER DOCUMENTS

Jacob Klapper and John T. Frankle, *Design of Compound and Multiple Loops for Low-Threshold Demodulation*, Academic Press, New York, 1972; Ch. 8.

The Commissioner is hereby authorized to charge any fees which may be required in this application under 37 C.F.R. §§1.16-1.17 to Deposit Account No. 06-1135.

Pursuant to 37 CFR §1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR §1.56(b).

Respectfully submitted,

FITCH, EVEN, TABIN & FLANNERY



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Dated: September 10, 2001

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